

General Description

The AAT3673 BatteryManager is a highly integrated single-cell (4.2V) lithium-ion/polymer battery charger and system power management IC that enables simultaneous battery charging and system load management. For increased safety, the AAT3673 includes over-voltage input protection (OVP) up to 28V.

The AAT3673 provides charging current and system power management from a single input that may be supplied by an AC adapter or USB port power source (ADP). This device allows the user to program the battery charge current up to 1.6A depending on the current shared with the system output. A battery charge timeout timer is provided for charging safety and the charge termination current is also user-programmable.

The AAT3673 employs a battery charge current reduction function that enables continued system operation in the event the input source can not supply the required load current. When operated under excessive thermal conditions, the AAT3673 has a digitally controlled thermal loop which allows the maximum possible charging current for any given ambient temperature condition.

Battery temperature, voltage and charge state are monitored for fault conditions. The AAT3673-1/-2/-4/-5 has two status monitor output pins (STAT1 and STAT2), and the AAT3673-3/-6 has one status monitor output (STAT1) provided to indicate battery charge status by directly driving external LEDs.

The AAT3673 is available in a Pb-free, thermally enhanced, space-saving 16-pin 4x4mm TDFN package.

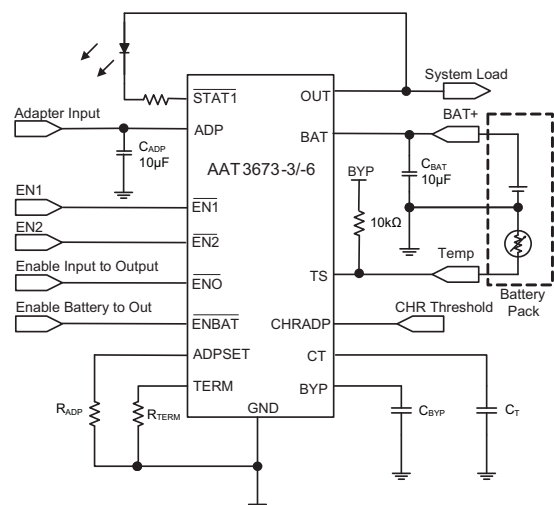
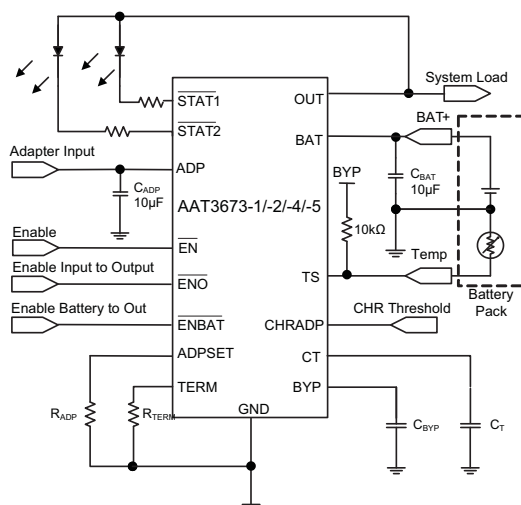
Features

- System Load Power Control from Either ADP or Battery
- ADP Presence Automatically Routes Power from Source to Load and Charges Battery
- Automatic Charge Reduction Loop to Minimize Charge Time with USB Input
- 4.0V~6.5V Input Voltage Range
- Over-Voltage Input Protection (OVP) up to 28V
- Digitally Controlled Thermal Protection
- Battery Power Enable
- Programmable Battery Charge Timer
- Battery Cell Temperature Sensing
- Charge Status Reporting (LEDs)
- Automatic Recharge Sequencing
- Battery Over-Voltage, Over-Current, and Over-Temperature Protection
- System Load Current Limiting
- 16-pin 4x4mm TDFN Package

Applications

- Cellular Phones
- Digital Still Cameras
- Digital Video Cameras
- Global Positioning Systems (GPS)
- MP3 Players
- Handheld PCs

Typical Application



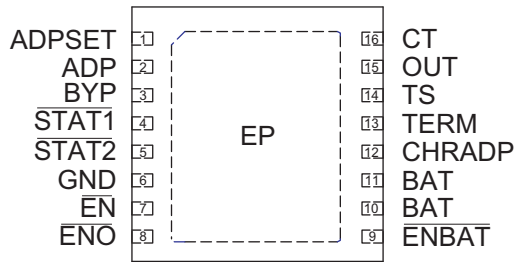
Pin Descriptions

Pin #	Name	Type	Function
1	ADPSET	I	Connect a resistor from this pin to GND to set the ADP fast charge constant current. The programmed constant current level should be less than the ADP current limit set by ADPLIM specification (I_{LIM_ADP}).
2	ADP	I	Adapter input, source of system load and battery charging. Connect a 1 μ F (minimum) ceramic capacitor as close as possible between ADP and GND.
3	BYP	I	Input for the over-voltage protection bypass node. Connect a 1 μ F (minimum) ceramic capacitor between this pin and GND.
4	$\overline{STAT1}$	O	This open-drain MOSFET device is for charger status reporting. If used for status indication display, connect an LED Cathode to this node with a series ballast resistor. Connect the LED anode to OUT or BYP.
5	$\overline{STAT2}$	O	AAT3673-1/-2/-4/-5: This open-drain MOSFET device is for charger status reporting. If used for status indication display, connect an LED cathode to this node with a series ballast resistor. Connect the LED anode to OUT or BYP.
	$\overline{EN2}$	I	AAT3673-3/-6: The $\overline{EN2}$ pin (internal pull-up) is used together with the $\overline{EN1}$ pin; see Table 2 in the "Functional Description" section of this datasheet.
6	GND	I/O	Common ground connection.
7	\overline{EN}	I	AAT3673-1/-2/-4/-5: Input enable (internal pull-up). Low to enable the ADP switch and battery charging path; high to disable the ADP switch and battery charging function. See Table 1 in the "Functional Description" section of this datasheet.
	$\overline{EN1}$	I	AAT3673-3/-6: The $\overline{EN1}$ pin (internal pull-up) is used together with the $\overline{EN2}$ pin; see Table 2 in the "Functional Description" section of this datasheet.
8	\overline{ENO}	I	Enable Input power to OUT, the dynamic power path from the ADP input to the system load. Active low input (internal pull-up).
9	\overline{ENBAT}	I	Battery load switch enable, active low. Battery load switch control the power path between the battery cell and OUT (internal pull-up).
10, 11	BAT	I/O	Battery pack (+) connection. For best operation, a 1 μ F (minimum) ceramic capacitor should be placed as close as possible between BAT and GND.
12	CHRADP	I	Adaptor mode charge reduction voltage threshold programming pin. The ADP charge reduction threshold may be adjusted from the default value by placing a voltage divider between this pin to VADP and GND to this pin.
13	TERM	I	Connect a resistor between this pin and GND to program the charge termination current threshold. The charge termination current level can be disabled by connecting this pin to BYP.
14	TS	I	Battery temperature sensing input. For typical applications, connect a 10k Ω resistor from BYP to this pin and a 10k Ω NTC thermistor located inside the battery pack under charge to this pin and GND to sense battery over temperature conditions during the charge cycle. To disable the TS function, connect this pin to GND.
15	OUT	O	System dynamic power output supplied from the ADP input, BAT or both. Connect a 10 μ F ceramic capacitor between this pin and GND.
16	CT	I	Battery charge timer input pin, connect a capacitor on this pin to set the ADP charge timers. Typically, a 0.1 μ F ceramic capacitor is connected between this pin and GND. To disable the timer circuit function, connect this pin directly to GND.
EP	EP	I/O	Exposed paddle (package bottom). Connect to GND plane under the device.

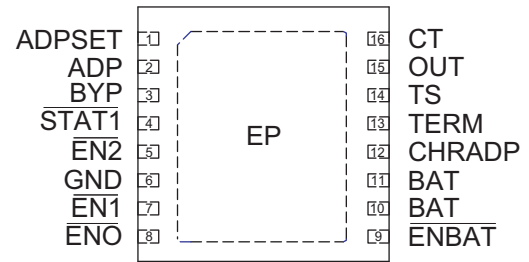
Pin Configuration

TDFN44-16
(Top View)

AAT3673-1/-2/-4/-5



AAT3673-3/-6



Absolute Maximum Ratings

Symbol	Description	Value	Units
V_{IN}	ADP Continuous	30	V
V_P	BAT, OUT, BYP <30ms, Duty Cycle < 10%	-0.3 to 8	V
	EN/EN1, ENO, ENBAT, STAT1, STAT2/EN2	-0.3 to 8	V
V_N	TS, CT, ADPSET, TERM, CHRADP	-0.3 to 8	V
T_J	Junction Temperature Range	-40 to 150	°C
T_A	Operating Temperature Range	-25 to 85	°C
T_{LEAD}	Maximum Soldering Temperature (at Leads, 10 sec)	300	°C

Thermal Information^{1, 2}

Symbol	Description	Value	Units
θ_{JA}	Maximum Thermal Resistance	46	°C/W
P_D	Maximum Power Dissipation	2.0	W

1. Mounted on 1.6mm thick FR4 circuit board.

2. Derate 50mW/°C above 25°C ambient temperature.

Electrical Characteristics

$V_{ADP} = 5V$, $T_A = -25^{\circ}C$ to $+85^{\circ}C$; unless otherwise noted, typical values are $T_A = 25^{\circ}C$.

Symbol	Description	Conditions	Min	Typ	Max	Units
Operation						
V_{OVP}	Input Over Voltage Protection Range				28	V
V_{ADP}	AC Adapter / USB Operating Voltage Range		4.0		6.5	V
V_{BAT}	Battery Operating Voltage Range		3.0		$V_{CO(REG)}$	V
V_{OUT}	ADP to OUT Voltage Regulation	$V_{ADP} = 5V$, $I_{OUT} = 100mA$, $T_A = 25^{\circ}C$	4.33	4.40	4.47	V
		$V_{ADP} = 4.8V$ to $6.5V$, $I_{OUT} = 0mA$ to $400mA$, $T_A = -25^{\circ}C$ to $+85^{\circ}C$	4.26	4.4	4.54	
V_{DO}	ADP to OUT Regulator Dropout ¹	$I_{OUT} = 400mA$		150	250	mV
V_{UVLO_ADP}	ADP Under-Voltage Lockout	Rising Edge		3.1	3.9	V
		Hysteresis		0.1		
V_{UVLO_BAT}	BAT Under-Voltage Lockout	Rising Edge	2.8	2.9	3.0	V
		Hysteresis		0.1		
I_{ADP_OP}	ADP Normal Operating Current	$V_{ADP} = 5V$, $V_{EN} = 0V$, $I_{CC} = 1A$		0.8	1.6	mA
I_{ADP_SHDN}	ADP Shutdown Mode Current (OVP is Still Active)	$V_{ADP} = 5V$, $V_{EN} = 5V$, $V_{ENBAT} = 5V$, No Load			360	μA
I_{BAT_OP}	Battery Operating Current	$V_{BAT} = V_{CO(REG)}$, $V_{ADP} = GND$, $V_{ENBAT} = 5V$, No Load		100	250	μA
I_{BAT_SLP}	Battery Sleep Current	$V_{BAT} = V_{CO(REG)}$, $V_{ADP} = 5V$, $V_{EN} = V_{ENBAT} = 5V$		5	10	μA
I_{BAT_SHDN}	Leakage Current from BAT Pin	$V_{BAT} = V_{CO(REG)}$, $V_{ENBAT} = V_{ADP} = 5V$			2	μA
Over-Voltage Protection						
V_{OVPT}	Over-Voltage Protection Trip Voltage	V_{ADP} Rising Edge	6.5	6.75	7.0	V
		Hysteresis		100		
V_{DO_OVP}	Dropout Voltage Between ADP and BYP Pins	$V_{ADP} = 5V$, $I_{ADP} = 500mA$		100	200	mV
T_{RESPOV}	Over-Voltage Protection Response Time	Step up V_{ADP} from 6V to 8V		0.5		μs
Options -1, -2, -3						
$T_{OVPOV_130\mu s}$	OVP Switch Release Delay Time	V_{ADP} voltage step down from 8V to 6V, $R_{LOAD} = 100\Omega$, $C_{BYP} = 1\mu F$		130		μs
$T_{OVPSARTON_130\mu s}$	OVP Switch Start Up Delay Time	V_{ADP} voltage step up from 0V to 5V, $R_{LOAD} = 100\Omega$, $C_{BYP} = 1\mu F$		150		μs
Options -4, -5, -6						
T_{OVPOV_80ms}	OVP Switch Release Delay Time	V_{ADP} voltage step down from 8V to 6V, $R_{LOAD} = 100\Omega$, $C_{BYP} = 1\mu F$		80		ms
$T_{OVPSARTON_80ms}$	OVP Switch Start Up Delay Time	V_{ADP} voltage step up from 0V to 5V, $R_{LOAD} = 100\Omega$, $C_{BYP} = 1\mu F$		80		ms
Power Switches						
$R_{DS(ON)_SWA}$	ADP-to-OUT FET On Resistance	$V_{ADP} = 5.0V$		600		m Ω
$R_{DS(ON)_SWB}$	BAT-to-OUT FET On Resistance	$V_{BAT} = 4.1V$		60		m Ω
$R_{DS(ON)_CHA}$	ADP Battery Charging FET On-Resistance	$V_{ADP} = 5.0V$		600		m Ω
Battery Charge Voltage Regulation						
$V_{CO(REG)}$	Output Charge Voltage Regulation		4.158	4.20	4.242	V
V_{MIN}	Preconditioning Voltage Threshold		2.8	2.9	3.0	V
V_{RCH}	Battery Recharge Voltage Threshold		$V_{CO(REG)} - 0.17$	$V_{CO(REG)} - 0.1$	$V_{CO(REG)} - 0.05$	V
V_{CHR_TH}	Default ADP Charge Reduction Threshold	CHRADP Open; Reduce Charge Current When ADP is Below V_{CHR_TH}		4.5		V
V_{CHR_REG}	CHRADP Pin Voltage Accuracy	$V_{ADP} = 4.5V$	1.9	2.0	2.1	V

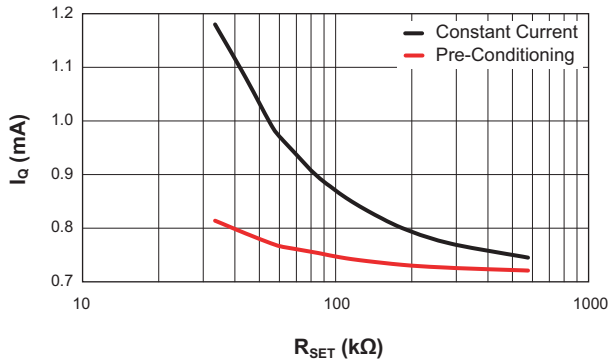
1. V_{DO} is defined as $V_{ADP} - V_{OUT}$ when V_{OUT} is 98% of normal.

Electrical Characteristics (continued)
 $V_{ADP} = 5V$, $T_A = -25^{\circ}C$ to $+85^{\circ}C$; unless otherwise noted, typical values are $T_A = 25^{\circ}C$.

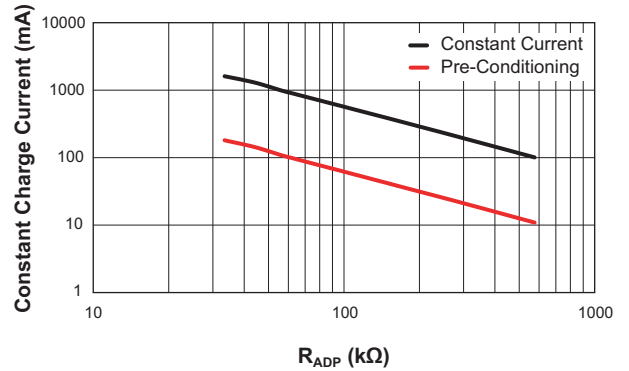
Symbol	Description	Conditions	Min	Typ	Max	Units
Current Regulation						
I_{LIM_ADP}	ADP Current Limit (Fixed)		1.6			A
I_{LIM_BAT}	BAT_OUT Current Limit (Fixed)		2.3			
$I_{CH_CC_ADP}$	ADP Charge Constant Current Charge Range		100		1600	mA
$\frac{\Delta I_{CH_CC_ADP}}{I_{CH_CC_ADP}}$	Constant Current Charge Current Regulation Tolerance	$I_{CH_CC_ADP} = 1A$	-12		12	%
$I_{CH_TKL_ADP}$	ADP Trickle Charge Current			10		% $I_{CH_CC_ADP}$
V_{ADPSET}	ADPSET Pin Voltage Regulation			2		V
V_{TERM}	TERM Pin Voltage Regulation			2		V
$K_{I_CC_ADP}$	Constant Current Charge Current Set Factor: I_{CH_ADP}/I_{ADPSET}			29300		V
K_{I_TERM}	Termination Current Set Factor: I_{CH_TERM}/I_{TERM}			2000		V
AAT3673-3 Only						
I_{CH_LO}	USB Low Level Charge Current (Fixed)	$V_{EN1} = V_{EN2} = 0$		85	100	mA
I_{CH_HI}	USB High Level Charge Current (Fixed)	$V_{EN1} = 0; V_{EN2} = 5V$		450	500	mA
Logic Control/Protection						
V_{EN}	Input High Threshold		1.6			V
	Input Low Threshold				0.4	
V_{STATx}	Output Low Voltage	STATx Pin Sinks 8mA			0.4	
T_C	Fast Charge (Trickle Charge + Constant Current + Constant Voltage Charges Together) Timeout	$C_{CT} = 0.1\mu F$		7		Hour
T_{TKL}	Trickle Charge Timeout			$T_C/8$		Min
V_{OVP}	Battery Over-Voltage Protection Threshold		$V_{CO(REG)} + 0.1$	$V_{CO(REG)} + 0.15$	$V_{CO(REG)} + 0.2$	V
I_{OCP}	Battery Charge Over-Current Protection Threshold	In All Modes		100		% I_{CH_CC}
TS1	High Temperature Threshold	Threshold	28	30	32	% V_{BYP}
TS2	Low Temperature Threshold	Threshold	58	60	62	% V_{BYP}
T_{LOOP_IN}	Digital Thermal Loop Entry Threshold			115		$^{\circ}C$
T_{LOOP_OUT}	Digital Thermal Loop Exit Threshold			95		$^{\circ}C$
T_{LOOP_REG}	Digital Thermal Loop Regulated Temperature			100		$^{\circ}C$
T_{SHDN}	Chip Thermal Shutdown Temperature	Threshold		140		$^{\circ}C$
		Hysteresis		15		$^{\circ}C$

Typical Characteristics

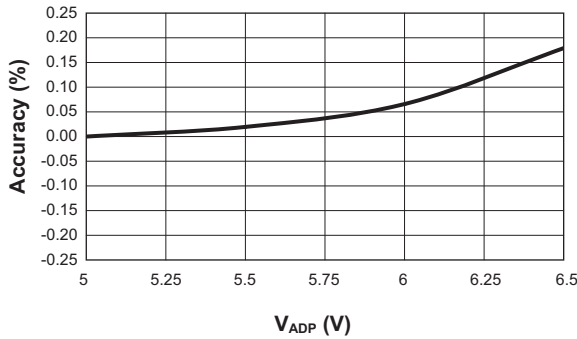
Adapter Supply Operating Current vs. R_{ADPSET}



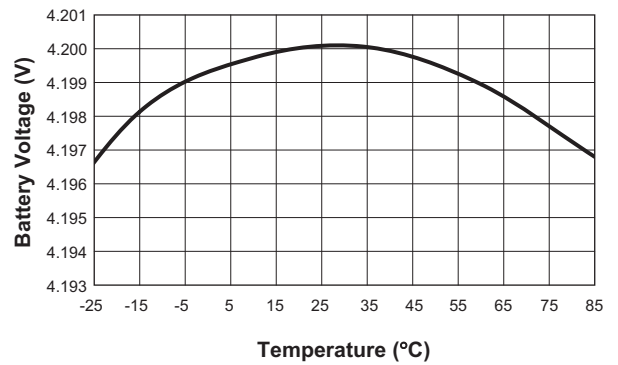
Constant Charge Current vs. R_{ADP}



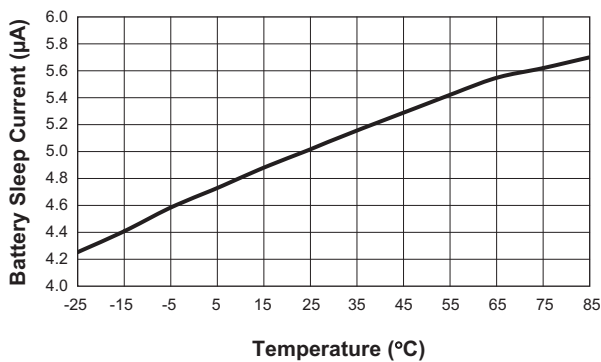
Output Charge Voltage Regulation Accuracy vs. Adapter Voltage
($V_{CO(REG)} = 4.2V$)



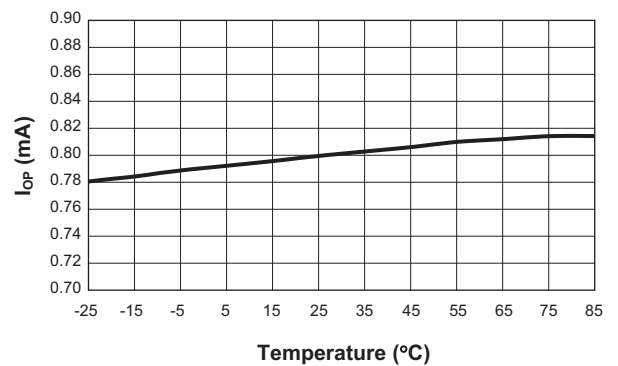
Constant Output Charge Voltage vs. Temperature



Battery Sleep Current vs. Temperature

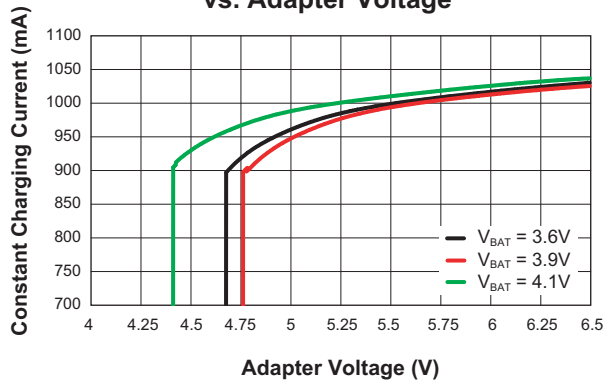


Operating Current vs. Temperature

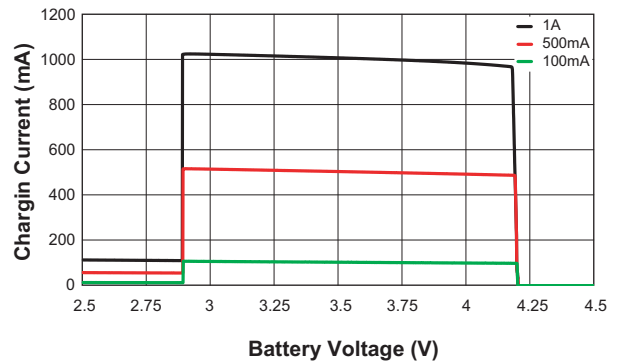


Typical Characteristics

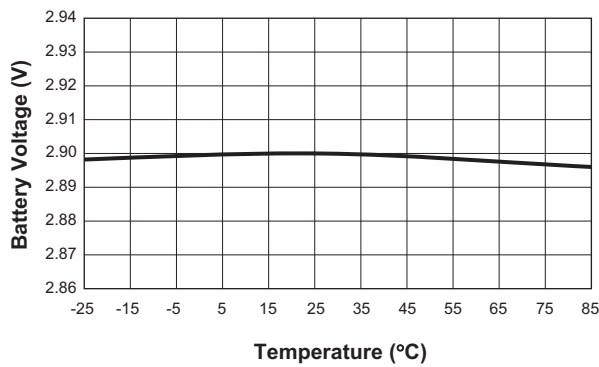
Constant Charging Current vs. Adapter Voltage



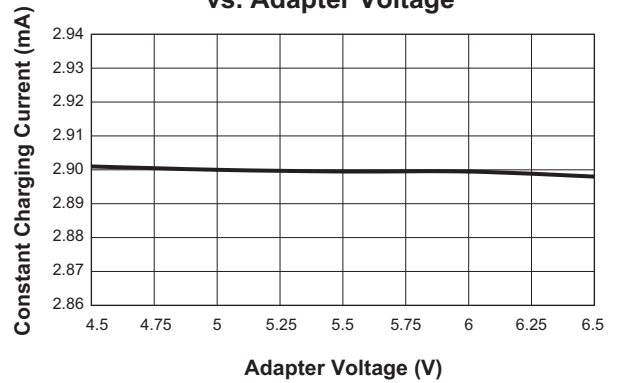
Charging Current vs. Battery Voltage



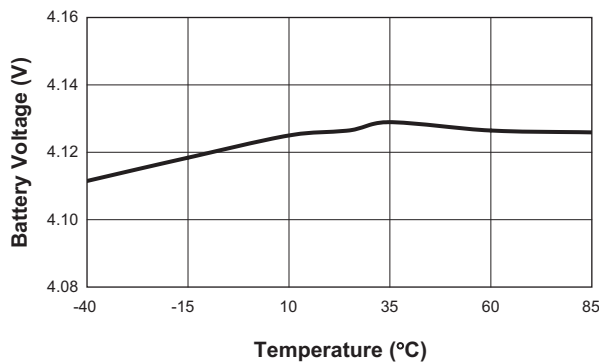
Preconditioning Voltage Threshold vs. Temperature



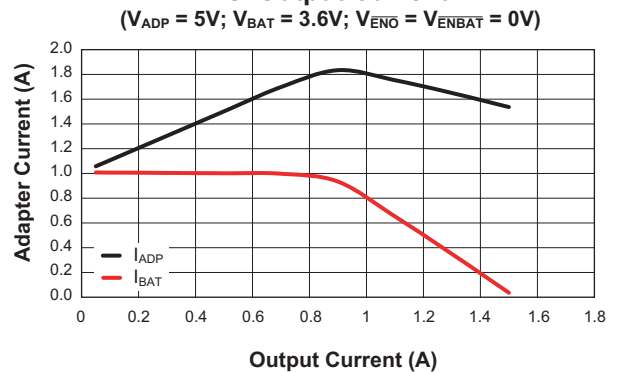
Preconditioning Voltage Threshold vs. Adapter Voltage



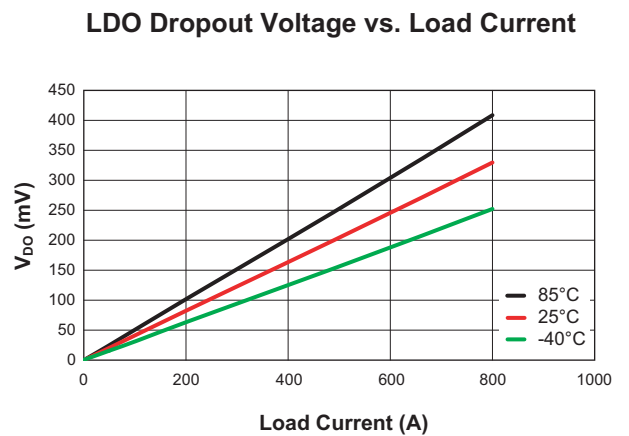
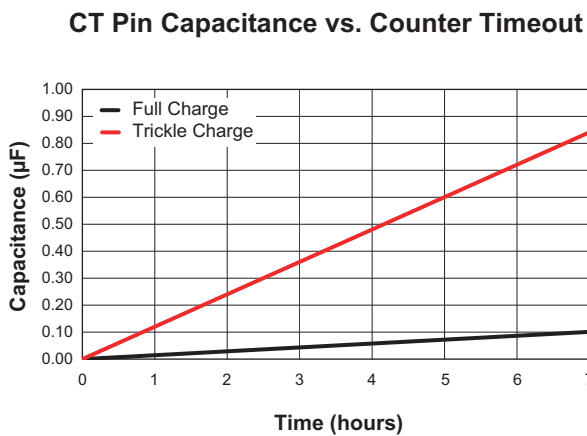
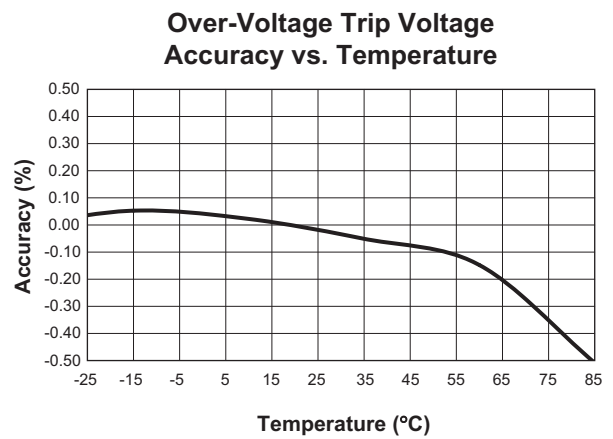
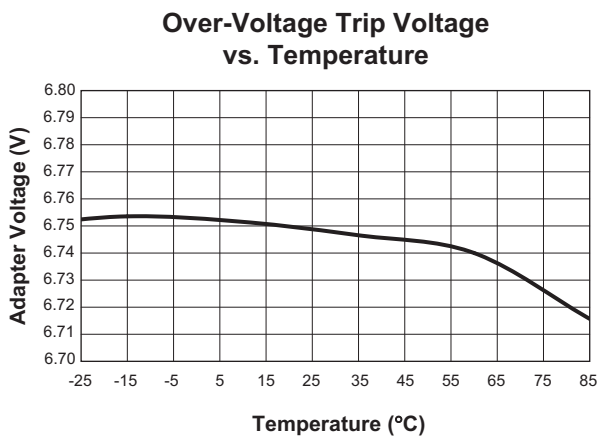
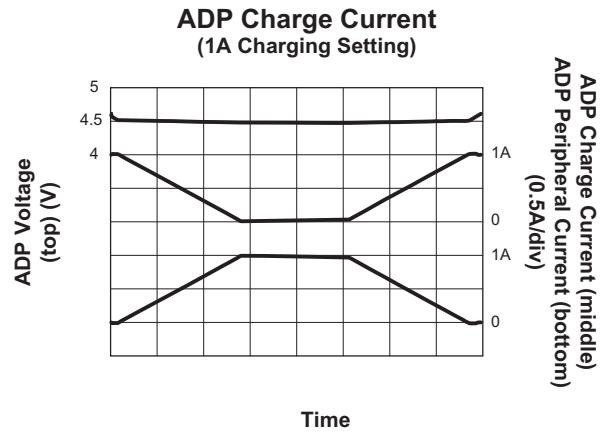
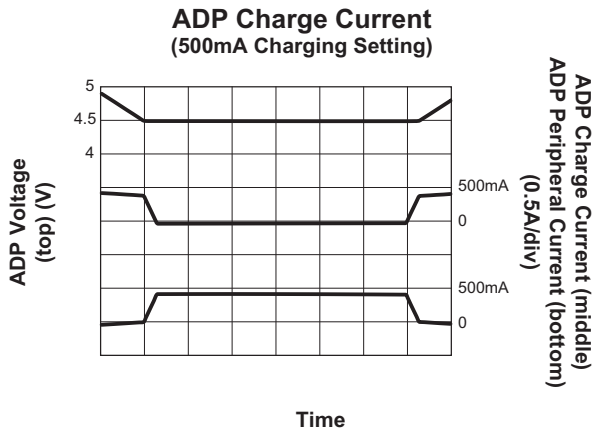
Recharge Voltage Threshold vs. Temperature
($V_{ADP} = 5V$; $R_{SET} = 56.7k\Omega$)



Adapter Charging Current vs. Output Current

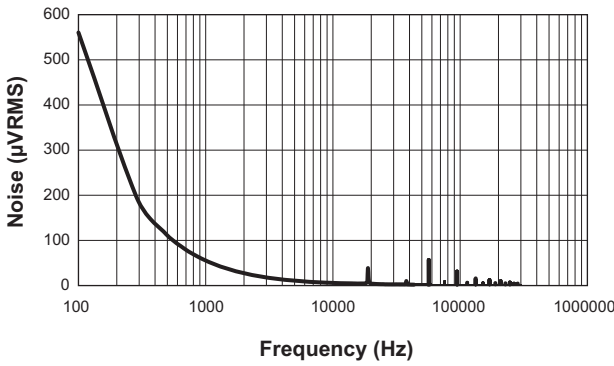


Typical Characteristics

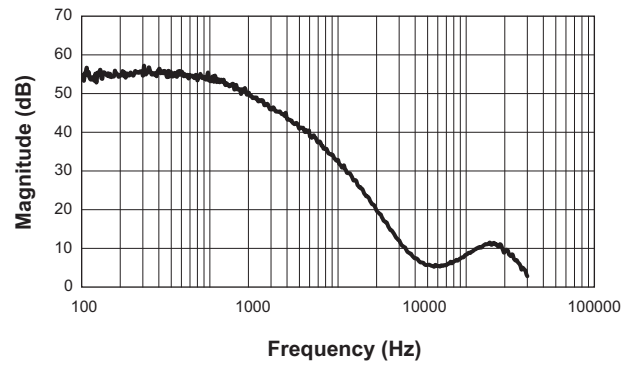


Typical Characteristics

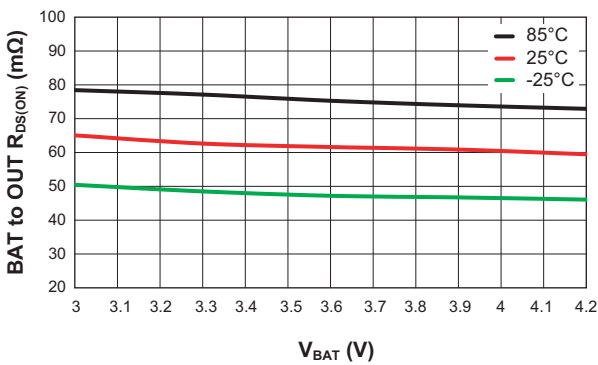
LDO Output Voltage Noise
($I_{OUT} = 10\text{mA}$, Power BW: 100Hz to 300KHz)



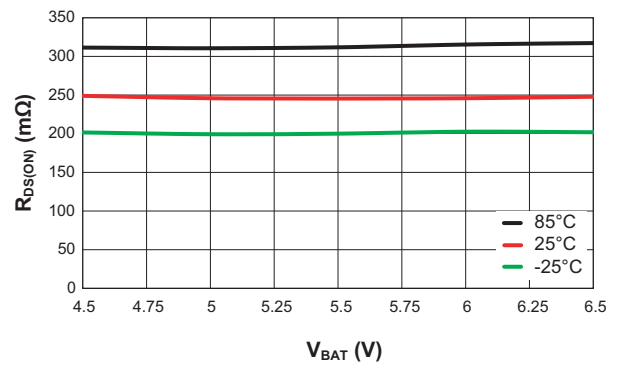
LDO Power Supply Rejection Ratio, PSRR
($V_{ADP} = 5\text{V}$, $I_{OUT} = 10\text{mA}$, BW: 50Hz to 300KHz)



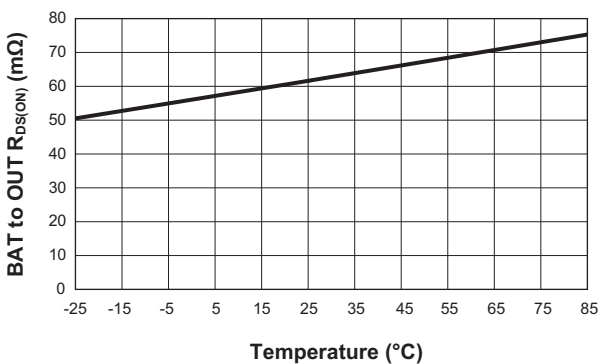
BAT to OUT $R_{DS(ON)}$ vs. Battery Voltage



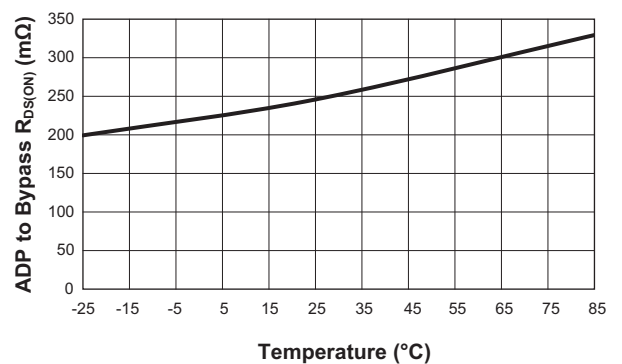
ADP to BYP $R_{DS(ON)}$ vs. Battery Voltage



BAT to OUT $R_{DS(ON)}$ vs. Temperature

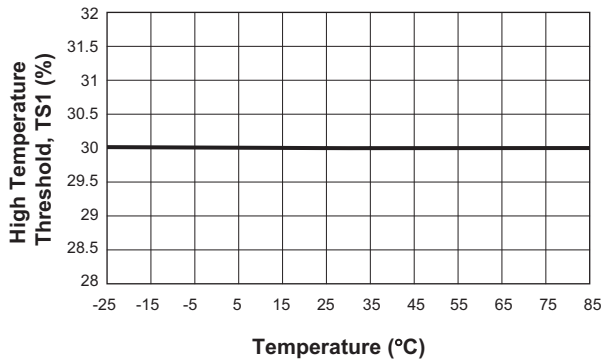


ADP to BYP $R_{DS(ON)}$ vs. Temperature

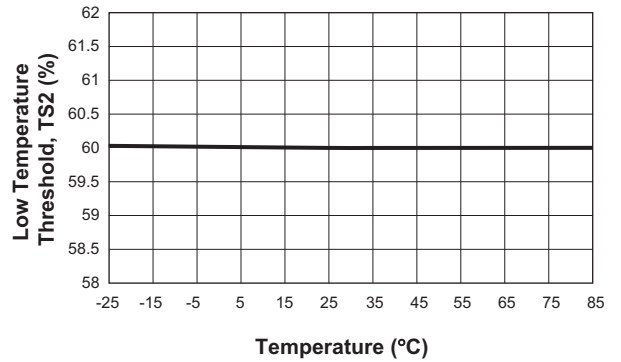


Typical Characteristics

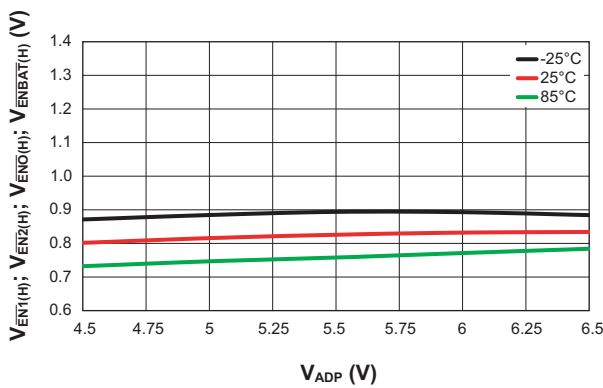
High Temperature Threshold
($V_{ADP} = 5V$)



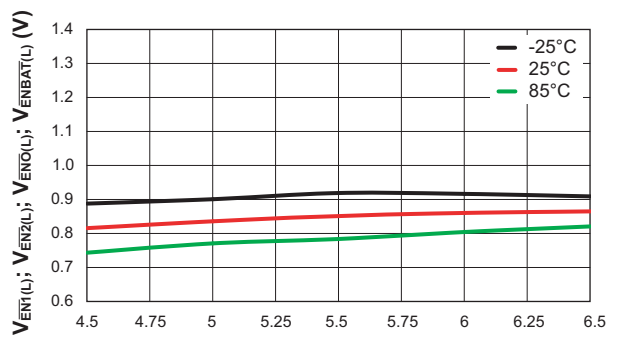
Low Temperature Threshold
($V_{ADP} = 5V$)



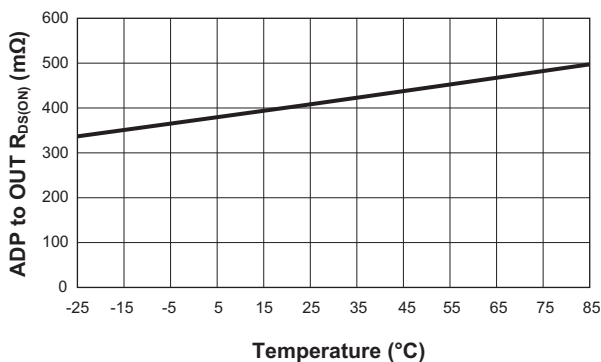
Input High Threshold vs. Adapter Voltage



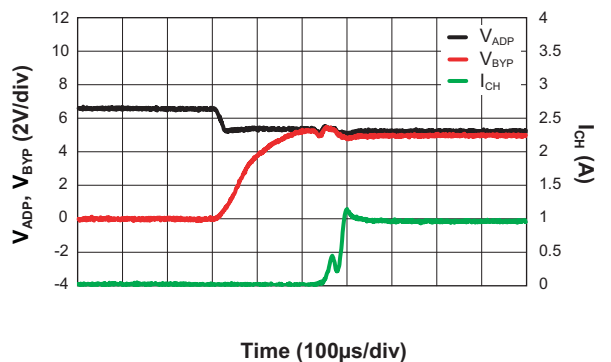
Input Low Threshold vs. Adapter Voltage



ADP to OUT $R_{DS(ON)}$ vs. Temperature



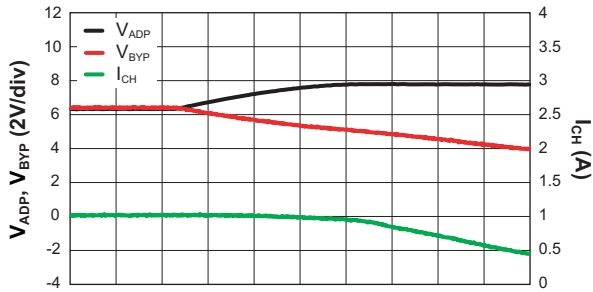
OVP Switch Turn-On Time
($V_{ADP} = 6.75V \rightarrow 5V$; $V_{OVPT} = 6.75V$; $I_{CH} = 1A$)



Typical Characteristics

OVP Event Transient

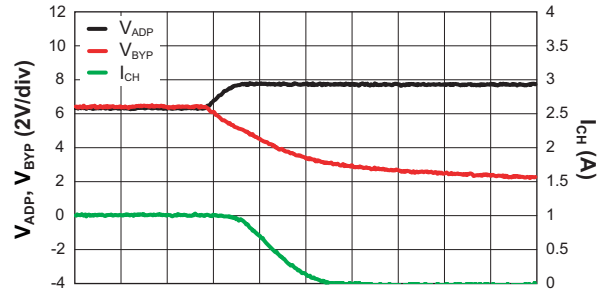
($V_{ADP} = 6.3V \rightarrow 8V$; $V_{OVPT} = 6.75V$; $I_{CH_CC} = 1A$)



Time (1µs/div)

OVP Event Transient

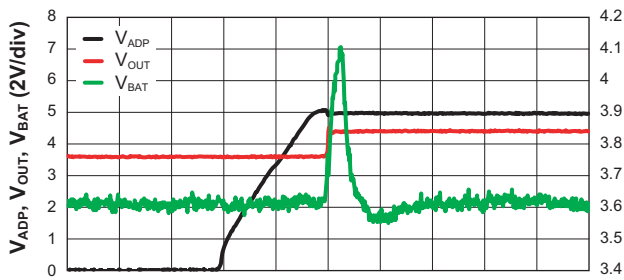
($V_{ADP} = 6.3V \rightarrow 8V$; $V_{OVPT} = 6.75V$; $I_{CH_CC} = 1A$)



Time (5µs/div)

Response of Out when Switching from V_{BAT} to V_{ADP}

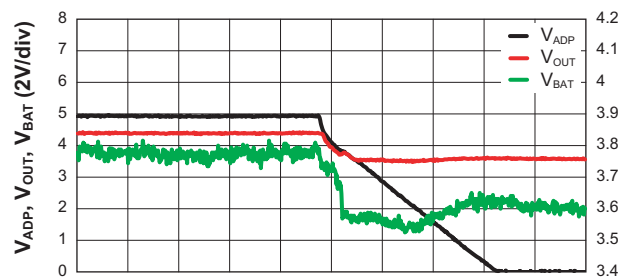
($V_{ADP} = 0V \rightarrow 5V$; $V_{BAT} = 3.6V$; $R_{LOAD} = 50\Omega$;
 $\overline{EN}_{BAT} = 0V$; $\overline{EN}_O = 0V$; $C_{OUT} = 10\mu F$)



Time (100µs/div)

Response of Out when Switching from V_{ADP} to V_{BAT}

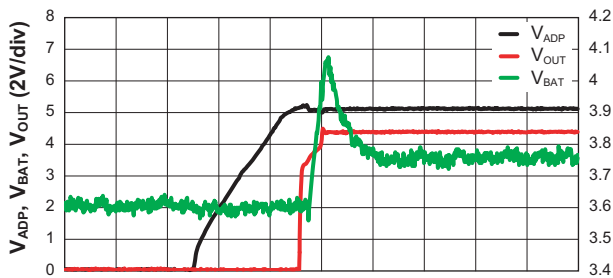
($V_{ADP} = 5V \rightarrow 0V$; $V_{BAT} = 3.6V$; $R_{LOAD} = 50\Omega$;
 $\overline{EN}_{BAT} = 0V$; $\overline{EN}_O = 0V$; $C_{OUT} = 10\mu F$)



Time (200µs/div)

Response of Out when Switching from V_{BAT} to V_{ADP}

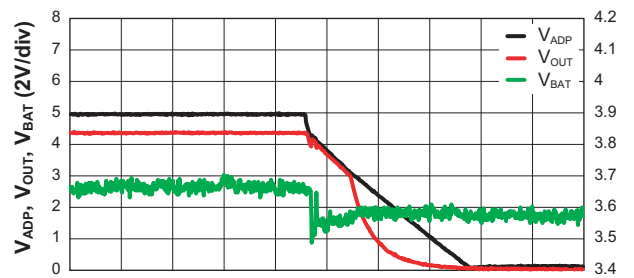
($V_{ADP} = 0V \rightarrow 5V$; $V_{BAT} = 3.6V$; $R_{LOAD} = 50\Omega$;
 $\overline{EN}_{BAT} = 5V$; $\overline{EN}_O = 0V$; $C_{OUT} = 10\mu F$)



Time (200µs/div)

Response of Out when Switching from V_{ADP} to V_{BAT}

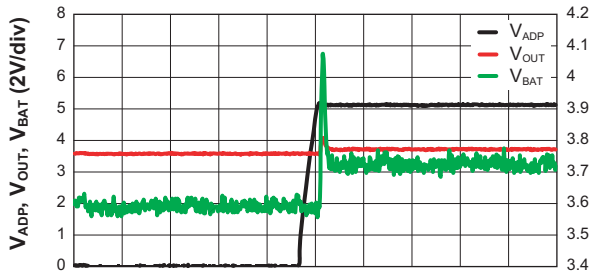
($V_{ADP} = 5V \rightarrow 0V$; $V_{BAT} = 3.6V$; $R_{LOAD} = 50\Omega$;
 $\overline{EN}_{BAT} = 5V$; $\overline{EN}_O = 0V$; $C_{OUT} = 10\mu F$)



Time (1ms/div)

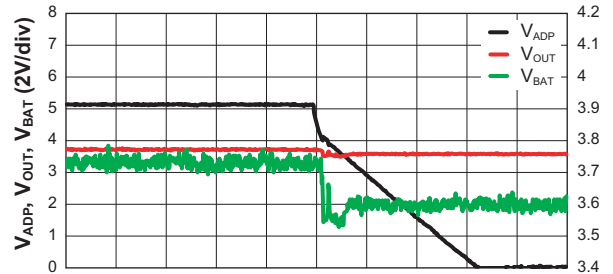
Typical Characteristics

Response of Out when $\overline{EN}_O = 5V$
 $(V_{ADP} = 0V \rightarrow 5V; V_{BAT} = 3.6V; R_{LOAD} = 50\Omega;$
 $\overline{EN}_{BAT} = 0V; C_{OUT} = 10\mu F)$



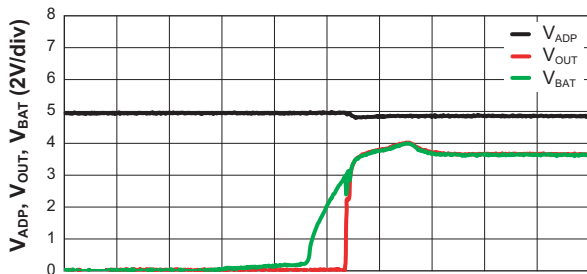
Time (1ms/div)

Response of Out when $\overline{EN}_O = 5V$
 $(V_{ADP} = 5V \rightarrow 0V; V_{BAT} = 3.6V; R_{LOAD} = 50\Omega;$
 $\overline{EN}_{BAT} = 0V; C_{OUT} = 10\mu F)$



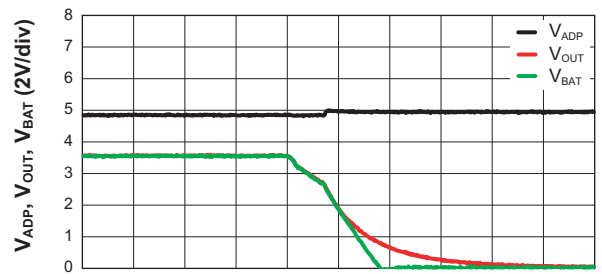
Time (1ms/div)

Response of Out when Inserting Battery
 $(V_{BAT} = 0V \rightarrow 3.6V; V_{ADP} = 5V; R_{LOAD} = 50\Omega;$
 $\overline{EN}_{BAT} = 0V; \overline{EN}_O = 0V; C_{OUT} = 10\mu F)$



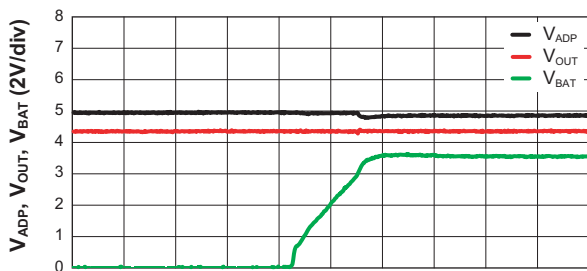
Time (200µs/div)

Response of Out when Removing Battery
 $(V_{BAT} = 4.1V \rightarrow 0V; V_{ADP} = 5V; R_{LOAD} = 50\Omega;$
 $\overline{EN}_{BAT} = 0V; \overline{EN}_O = 5V; C_{OUT} = 10\mu F)$



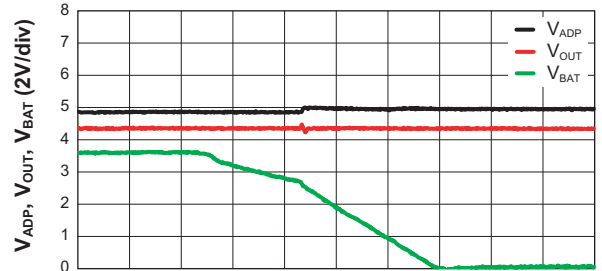
Time (500µs/div)

Response of Out when Inserting Battery
 $(V_{BAT} = 0V \rightarrow 3.6V; R_{LOAD} = 50\Omega; V_{ADP} = 5V;$
 $\overline{EN}_{BAT} = 5V; \overline{EN}_O = 0V; C_{OUT} = 10\mu F)$



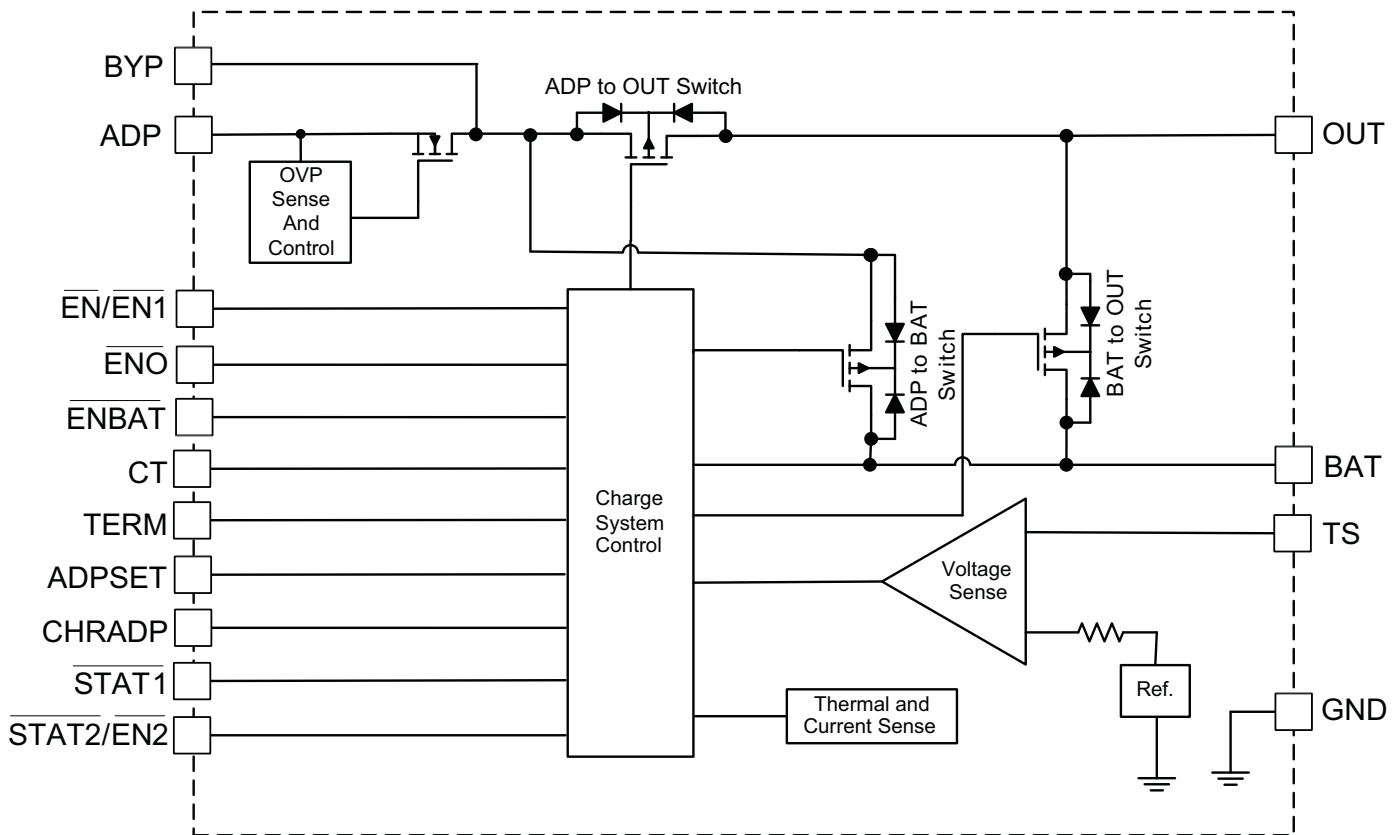
Time (200µs/div)

Response of Out when Removing Battery
 $(V_{BAT} = 4.1V \rightarrow 0V; V_{ADP} = 5V; R_{LOAD} = 50\Omega;$
 $\overline{EN}_{BAT} = 5V; \overline{EN}_O = 0V; C_{OUT} = 10\mu F)$



Time (500µs/div)

Functional Block Diagram



Functional Description

The AAT3673 is a single input dynamic battery charger and power control IC. The input power control is designed to be compatible with either AC power adapter or USB port power sources. In addition, this device also provides dynamic power control to charge a single cell Li-ion battery and power a system load simultaneously. The OUT pin provides regulated 4.4V when input requirement is met. If the input voltage is not sufficiently high to ensure regulated OUT, the output will track input assuming the drop through the switch ADP to BAT or BAT to OUT.

The device contains a charge regulation pass devices to control the charge current or voltage from the adapter input power to the battery, it also contains two additional load switches to control and route input power to supply the system load and manage power from the battery to the system load. This charge control and switch array permits dynamic charging of the battery cell and control of power to the system load simultaneously.

When an input power source is applied to the AAT3673, the adapter input will provide power to the system load and charge the battery. Without a valid supply present on the ADP pin, the battery will power the system load as long as the battery voltage is greater than 2.9V. The internal battery voltage sense circuit will disconnect the battery from the load if the cell voltage falls below 2.9V to protect the battery cell from over-discharge which results in shorter battery life.

The system load current drawn from the battery is limited internally. The AAT3673 precisely regulates battery charge current and voltage for 4.2V Li-ion battery cells. The battery charge current can be programmed up to 1.6A. During battery charge, the AAT3673 pre-conditions (trickle charges) the battery with a lower current when the battery voltage is less than 2.9V. The system then charges the battery in a constant current fast charge mode when the battery voltage is above 2.9V. When the battery voltage rises to 4.2V, the charger will automatically switch to a constant voltage mode until the charge current is reduced to the programmed charge termination current threshold.

The internal arrangement of load switches and the charge regulation device provide dynamic power sourcing to the system load. If the system load exceeds the input current supply from the input source, additional current can be supplied from the battery cell. At all times, the device will manage distribution of power between the source, the battery and the system simultaneously in order to support system power needs and charge the battery cell with the maximum amount of current possible. The AAT3673 has a unique internal charge current reduction control loop that will prevent an input source from overload. In the case of USB charging from a USB port V_{USB} supply, there are two events which need to be guarded against. The first is charging from a defective or inadequate USB host supply; the second problem could arise if the programmed charge current plus the system supply demand through the AAT3673 exceeds the ability of a given USB port. In either case, the AAT3673 charge reduction (CHR) loop will activate when the input source drops below the V_{CHR_TH} threshold of 4.5V. The CHR loop will automatically reduce the charge current to the battery until the supply voltage recovers to a point above the V_{CHR_TH} threshold. This unique feature protects the charger, system and source supply in the event an adapter or power source does not meet the programmed ADP charging mode current demand. The resulting CHR system will permit the charging of a battery cell with the maximum possible amount of charge current for any given source.

During battery charging, the device temperature can rise due to power dissipation within the charge current control device and the load switches. In some cases, the power dissipation in the device may cause the junction temperature to rise up to its thermal shutdown threshold. In the event of an internal over-temperature condition caused by excessive ambient operating temperature or an excessive power dissipation condition, the AAT3673 utilizes a digitally controlled thermal loop system that will reduce the charging current to prevent the device from entering thermal shutdown. The digital thermal loop will maintain the maximum possible battery charging current for the given set of input to output power dissipation and ambient temperature conditions.

The digital thermal loop control is dynamic in the sense that it will continue to adjust the battery charging current as operating conditions change. The digital thermal loop will reset and resume normal operation when the power dissipation or over temperature conditions are removed.

Battery temperature and charge state are fully monitored for fault conditions. In the event of an over voltage, over-current, or over-temperature failure, the device will automatically shut down, thus protecting the charging device, control system, and the battery under charge. In addition to internal charge controller thermal protection, the AAT3673 also provides a temperature sense feedback function (TS pin) from the battery to shut down the device in the event the battery exceeds its own thermal limit during charging. All fault events are reported to the user by the simple status LED(s) which is (are) internally controlled by open drain NMOS switch(es).

Charging Operation

The AAT3673 has four basic modes for the battery charge cycle: pre-conditioning/trickle charge, constant current fast charge, constant voltage, and end of charge/sleep state.

Battery Preconditioning

Before the start of charging, the AAT3673 checks several conditions in order to assure a safe charging environment. The input supply must be above the minimum operating voltage, or under-voltage lockout threshold (V_{UVLO}), for the charging sequence to begin. Also, the cell temperature, as reported by a thermistor connected to the TS pin from the battery, must be within the proper window for safe charging. When these conditions have been met and a battery is connected to the BAT pin, the AAT3673 checks the state of the battery by sensing the cell voltage. If the cell voltage is below the preconditioning voltage threshold (V_{MIN}), the AAT3673 begins preconditioning the battery cell with charge current which is 10% of the fast charge current.

Fast Charge/Constant Current Charging

Battery cell preconditioning continues until the voltage measured by the internal sense circuit exceeds the preconditioning voltage threshold (V_{MIN}). At this point, the AAT3673 begins the fast charge constant current phase. The fast charge constant current (I_{CH_CC}) level is programmed by the user via the R_{ADP} resistor. The AAT3673 remains in constant current charge mode until the battery reaches the voltage regulation point, $V_{CO(REG)}$. The formula for fast charge current as a function of current setting resistor is:

$$I_{CH_CC} = K_{I_CC_ADP} \cdot \left(\frac{2V}{R_{ADP}} \right)$$

Alternately, to select the resistor value for a given charging current use:

$$R_{ADP} = K_{I_CC_ADP} \cdot \left(\frac{2V}{I_{CH_CC}} \right)$$

where $K_{I_CC_ADP} = 29300$ (typical).

Constant Voltage Charging

The charge control system transitions to a regulated constant voltage phase from the constant current fast charge mode when the battery voltage reaches the end of charge regulation threshold ($V_{CO(REG)}$). The regulation voltage level is factory programmed to 4.2V ($\pm 1\%$). The charge current in the constant voltage mode drops as the battery cell under charge reaches its maximum capacity.

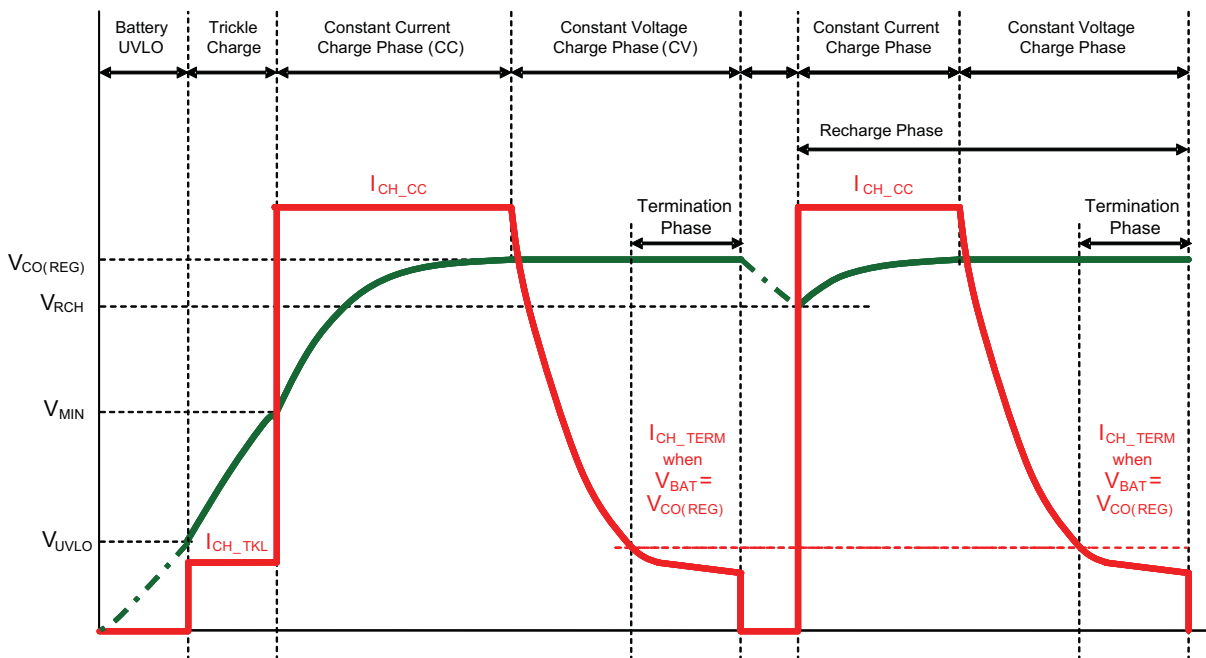


Figure 1: Current vs. Voltage and Charger Time Profile.

Control Inputs			Pass Devices		
$\overline{\text{EN}}$	$\overline{\text{ENO}}$	$\overline{\text{ENBAT}}$	ADP - OUT	ADP - BAT	BAT - OUT
1	1	1	OFF	OFF	OFF
0	1	1	OFF	ON	OFF
1	0	1	OFF	OFF	OFF
0	0	1	ON	ON	OFF
1	1	0	OFF	OFF	ON
0	1	0	OFF	ON	ON
1	0	0	OFF	OFF	ON
0	0	0	ON	ON	ON

Table 1: AAT3673-1/-4 and AAT3673-2/-5 Battery and Adapter Dynamic Path Control Table.

Control Inputs				Pass Devices		
$\overline{\text{EN1}}$	$\overline{\text{EN2}}$	$\overline{\text{ENO}}$	$\overline{\text{ENBAT}}$	ADP-OUT	ADP-BAT	BAT-OUT
1	1	1	1	OFF	OFF	OFF
0	0	1	1	OFF	ON	OFF
0	1	1	1	OFF	ON	OFF
1	0	1	1	OFF	ON	OFF
1	1	0	1	OFF	OFF	OFF
0	0	0	1	ON	ON	OFF
0	1	0	1	ON	ON	OFF
1	0	0	1	ON	ON	OFF
1	1	1	0	OFF	OFF	ON
0	0	1	0	OFF	ON	ON
0	1	1	0	OFF	ON	ON
1	0	1	0	OFF	ON	ON
1	1	0	0	OFF	OFF	ON
0	0	0	0	ON	ON	ON
0	1	0	0	ON	ON	ON
1	0	0	0	ON	ON	ON

Table 2: AAT3673-3/-6 Battery and Adapter Dynamic Path Control Table

End of Charge Cycle Termination and Recharge Sequence

When the charge current drops to the user programmed charge termination current at the end of the constant voltage charging phase, the device terminates charging, enables the recharge control circuit and enters the sleep state. The charger will remain in the sleep state until the battery voltage decreases to a level below the battery recharge voltage threshold (V_{RCH}). The charge termination current is programmed via the R_{TERM} resistor which is connected between the TERM pin and ground. Use the values listed in Table 3 to set the desired charge termination current. The programmed charge termination current will remain at the same set level regardless of which fast charge ADP, USBH or USBL constant current mode is selected.

I_{TERM} (mA)	R_{TERM} (k Ω)
320	11.0
174	21.0
125	30.9
95	41.2
77	51.1
64	61.9
58	71.5
50	80.6
49	90.9
42	100.0
37	110.0

Table 3: Charge Termination Current Programming Resistor Values.

If the desired end of charge termination current level is not listed in Table 3, the TERM resistor value may be calculated by the following equation:

$$I_{CH_TERM} = K_{I_TERM} \cdot \left(\frac{2V}{R_{TERM}} \right)$$

or

$$R_{TERM} = K_{I_TERM} \cdot \left(\frac{2V}{I_{CH_TERM}} \right)$$

$K_{I_TERM} = 2000$ (typical)

When the input supply is disconnected, the charger also automatically enters power-saving sleep mode. Consuming less than 1µA in sleep mode, the AAT3673 minimizes battery drain when not charging. This feature is particularly useful in applications where the input supply level may fall below the usable range of the charge reduction control or under-voltage lockout level. In such cases where the AAT3673 input voltage drops, the device will enter the sleep mode and automatically resume charging once the input supply has recovered from its fault condition.

Current Regulation

The ADP current limit (I_{LIM_ADP}) = BAT_OUT current (I_{LIM_BAT}) + ADP fast charge (CC) current (I_{CH_CC}). For example: if ADP fast charge current is set to 0.6A, then the BAT_OUT current is 1A. If the BAT_OUT current increases to 1.2A, then the ADP fast charge current is reduced to 0.4A because ADP current limit is 1.6A. However, the 1.6A number is the minimum value for the current limit, not the typical value.

Over-Voltage Protection

In normal operation, an N-channel MOSFET acts as a slew-rate controlled load switch, connecting and disconnecting the power supply from ADP to BYP. A low resistance MOSFET is used to minimize the voltage drop between the voltage source and the charger and to reduce the power dissipation. When the voltage on the input exceeds the over-voltage trip point of 6.75V, the device turns off the internal switch which disconnects the charger from the abnormal input voltage, therefore pre-

venting any damage to the charger. The OVP turn-on and release delay times for the AAT3673-1/-2/-3 are 150µs and 130µs respectively, while in the AAT3673-4/-5/-6 these delay times are extended to 80ms typically. If an over-voltage condition is applied at the time of the device enable, then the load switch will remain OFF.

OVP Under-Voltage Lockout (UVLO)

The AAT3673 OVP circuitry has a fixed 3.1V under-voltage lockout level (UVLO). When the adapter input voltage is less than the UVLO threshold level, the MOSFET load switch is turned off. A 100mV of hysteresis is included to ensure circuit stability.

Temperature Sense (TS)

Inside the AAT3673, the internal battery temperature sensing circuit is comprised of two comparators which establish a voltage window for safe operation. The thresholds for the TS operating window are bounded by the TS1 and TS2 specifications. Referring to the Electrical Characteristics table in this datasheet, the TS1 threshold = 30% · V_{BYP} and the TS2 threshold = 60% · V_{BYP} . If the use of the TS pin function is not required by the system, it should be terminated to ground.

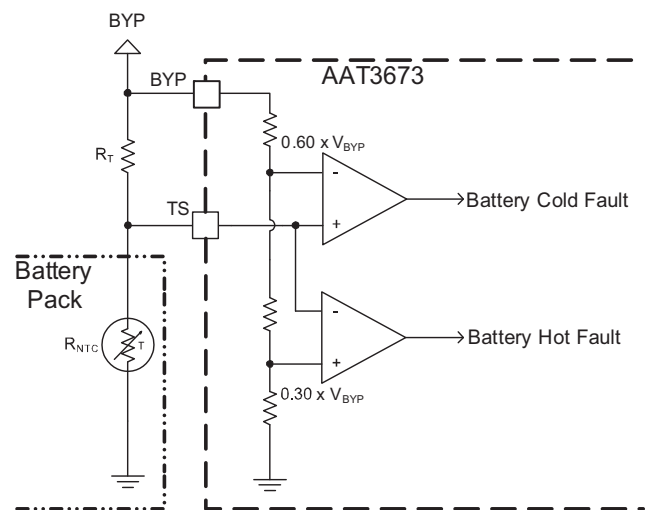


Figure 2: AAT3673 Battery Temperature Sensing Circuit.

Charge Safety Timer (CT)

While monitoring the charge cycle, the AAT3673 utilizes a charge timer to help identify damaged cells and to ensure that the cell is charged safely. Operation is as follows: upon initiating a charging cycle, the AAT3673 charges the cell at 10% of the programmed maximum charge until $V_{BAT} > 2.9V$. If the cell voltage fails to reach the preconditioning threshold of 2.9V (typ) before the safety timer expires, the cell is assumed to be damaged and the charge cycle terminates. If the cell voltage exceeds 2.9V prior to the expiration of the timer, the charge cycle proceeds into fast charge. There are two timeout periods: 50 minutes for Trickle Charge mode, and 6 hours for Constant Current Mode and Constant Voltage mode altogether.

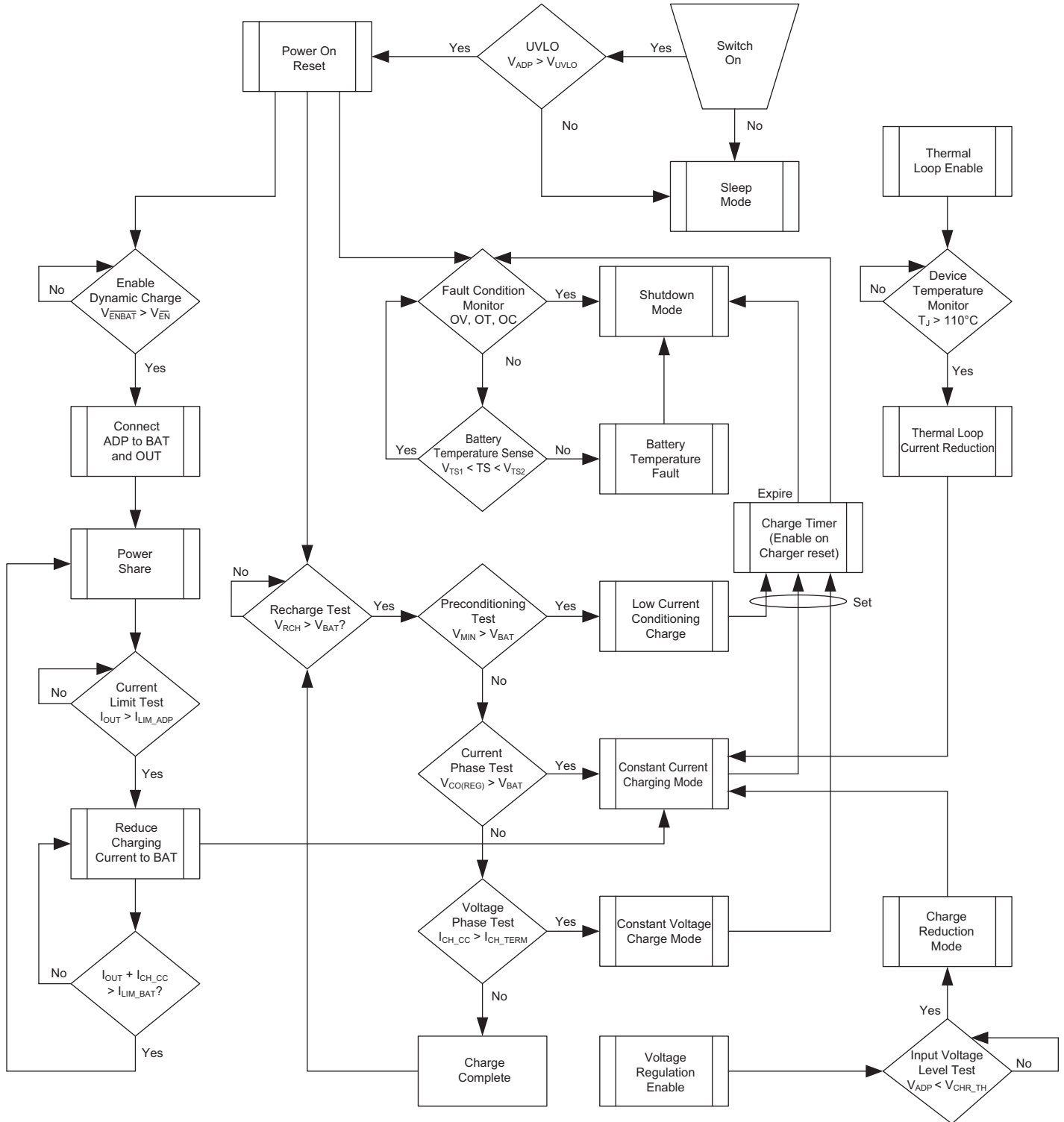
The CT pin is driven by a constant current source and will provide a linear response to increases in the timing capacitor value. The timeout is 7 hours (typical) using a 100nF capacitor for C_T . Thus, for a 200nF capacitor it would be 14 hours, and for a 50nF capacitor it would be 3.5 hours respectively.

For a given target delay time T_D (in hours) calculate:

$$C_T = \frac{(T_D \cdot 100nF)}{7}$$

If the programmable watchdog timer function is not needed, it can be disabled by terminating the CT pin to ground. The CT pin should not be left floating or unterminated, as this will cause errors in the internal timing control circuit. The constant current provided to charge the timing capacitor is very small, and this pin is susceptible to noise and changes in capacitance value. Therefore, the timing capacitor should be physically located on the printed circuit board layout as close as possible to the CT pin. Since the accuracy of the internal timer is dominated by the capacitance value, a 10% tolerance or better ceramic capacitor is recommended. Ceramic capacitor materials, such as X7R and X5R types, are a good choice for this application.

System Operation Flowchart



Applications Information

Adapter or USB Port Power Source

In the adapter mode, constant current charge levels up to 1.6A may be programmed by the user. The ADP input will operate over a range from 4.0V to 6.5V.

The constant fast charge current for the adapter input mode is set by the R_{ADP} resistor connected between the ADPSET pin and ground. The battery preconditioning or trickle charge current is fixed at 10% of the programmed fast charge constant current level. Refer to Table 4 for recommended R_{ADP} values for a desired constant current charge level. Please refer to the Battery Charge Status Indication discussion on page 21 of this datasheet for further details.

Charge Reduction

Under normal operation, the AAT3673 should be operated from an adapter power source with a sufficient capacity to supply the desired constant charge current plus any additional load which may be placed on the source by the operating system. In the event that the power source to the ADP pin is unable to provide the programmed fast charge constant current, or if the system under charge must also share supply current with other functions, the AAT3673 will automatically reduce the ADP fast charge current level to maintain the integrity of the source supply, power the operating system, and charge the battery cell with the remaining available current.

The ADP charge reduction system becomes active when the voltage on the ADP input falls below the ADP charge reduction threshold (V_{CHR_TH}), which is preset to 4.5V. Should the input supply drop below the V_{CHR_TH} threshold, the charge reduction system will reduce the fast charge current level in a linear fashion until the voltage sensed on the ADP input recovers to the charge reduction threshold voltage. The ADP charge reduction threshold (V_{CHR_TH}) may be externally set to a value other than 4.5V by placing a resistor divider network between the BYP pin and ground with the center connected to the CHRADP pin. The ADP charge reduction feature may be disabled by shorting the CHRADP pin directly to the BYP pin.

The following equation may be used to approximate the ADP charge reduction threshold above or below 4.5V:

$$V_{CHR_TH} = \frac{2.0V}{(R4/[R4 + R3])}$$

where $R4$ and $R3 \ll 500k\Omega$.

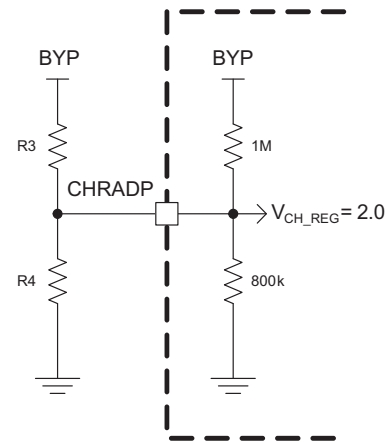


Figure 3: Internal Equivalent Circuit for the CHRADP Pin.

Adapter Input Charge Inhibit and Resume

The AAT3673 has an under-voltage lockout (UVLO) and power on reset feature to protect the charger IC in the event the voltage on the BYP pin drops below the UVLO threshold. Under a UVLO condition, the charger will suspend the charging process. When power is applied to the adapter pin again or the UVLO condition recovers, the system charge control will assess the state of charge on the battery cell and will automatically resume charging in the appropriate mode for the condition of the battery.

Programming Fast Charge Current

The constant current charge level is user programmable with a set resistor connected between the ADPSET pin and ground. The accuracy of the constant charge current, as well as the preconditioning trickle charge current, is dominated by the tolerance of the set resistor used. For this reason, a 1% tolerance metal film resistor is recommended for the set resistor function. The constant charge current levels from 100mA to 1.6A may be set by selecting the appropriate value from Table 4.

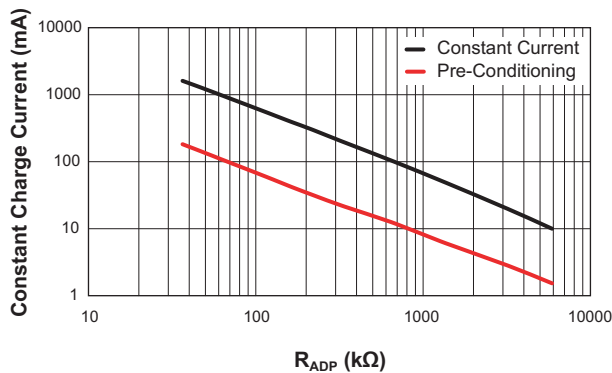
Charge current setting formula:

$$I_{CH_CC_ADP} (typ) = \frac{V_{ADP}}{R_{ADP}} \cdot KI_{I_CC_ADP}$$

Constant Charge Current (mA)	Set Resistor Value (kΩ)
100	576
200	287
300	191
400	143
500	113
800	69.8
1000	56.2
1600	33.4

Table 4: R_{ADP} Values.

Figure 4 shows the relationship of constant charging current and set resistor values for the AAT3673.


Figure 4: Constant Charging Current vs. Set Resistor Values.

For the AAT3673-3/-6, the two enable inputs select between four possible operating modes: two internally fixed charging current modes (USB Low = 100mA or USB high = 500mA), an externally programmable charging current mode, and a shutdown mode. The $\overline{\text{STAT1}}$ functionality is identical for all three modes and does not depend on the $\overline{\text{EN1}}$ and $\overline{\text{EN2}}$ enable inputs.

$\overline{\text{EN1}}$	$\overline{\text{EN2}}$	Operating Mode
0	0	USB Low, 100mA charging current
0	1	USB High, 500mA charging current
1	0	Using R _{ADP} to program charging current
1	1	Shutdown mode

Table 5: AAT3673-3/-6 Operating Modes.

Battery Connection (BAT)

A single cell Li-Ion/Polymer battery should be connected between BAT input and ground.

Battery Charge Status Indication

Charge Status Indicator Outputs

There are six device options, which are described in Table 11 on page 26. All options include recharge sequence after adapter is inserted. The AAT3673-1/-4 and AAT3673-2/-5 have two status ($\overline{\text{STAT1}}$ and $\overline{\text{STAT2}}$) pins and one enable pin ($\overline{\text{EN}}$); the AAT3673-3/-6 has one status pin ($\overline{\text{STAT1}}$) and two enable pins ($\overline{\text{EN1}}$ and $\overline{\text{EN2}}$).

Charge State	$\overline{\text{STAT1}}$	$\overline{\text{STAT2}}$
Pre-Charge	ON	ON
Fast-Charge	ON	OFF
Charge Complete	OFF	ON
Charge Disabled, Sleep Mode or Fault Condition	OFF	OFF
No Battery (with Charge Enabled)	Flash (1Hz, 40% duty)	Flash (1Hz, 40% duty)

Table 6: AAT3673-1/-4 LED Status Indicators.

Charge State	$\overline{\text{STAT1}}$	$\overline{\text{STAT2}}$
Pre-Charge or Fast-Charge	ON	OFF
Charge Complete, Charge Disabled, or Sleep Mode	OFF	OFF
Fault Condition	OFF	ON
No Battery (with Charge Enabled)	Flash (1Hz, 40% duty)	OFF

Table 7: AAT3673-2/-5 LED Status Indicators.

Charge State	$\overline{\text{STAT1}}$
Pre-Charge or Fast-Charge	ON
Charge Complete, Charge Disabled, Sleep Mode, or Fault Condition	OFF
No Battery (with Charge Enabled)	Flash (1Hz, 40% duty)

Table 8: AAT3673-3/-6 LED Status Indicators.

Fault condition can be one of the following:

- Battery over-voltage (OV)
- Battery temperature sense hot or cold
- Battery charge timer time-out
- Chip thermal shutdown

Status Indicator Display

Simple system charging status states can be displayed using one LED each in conjunction with the $\overline{\text{STAT1}}$ and $\overline{\text{STAT2}}$ pins of the AAT3673-1/-2/-4/-5 and the $\overline{\text{STAT1}}$ pin of the AAT3673-3/-6. These pins have simple switches connecting the LED's cathodes to ground. Refer to Tables 6, 7, and 8 for LED display definitions. The LED anodes should be connected to BYP or other system power that does not exceed 6.5V, depending upon system design requirements. The LED should be biased with as little current as necessary to create reasonable illumination; therefore, a ballast resistor should be placed between the LED cathode and the $\overline{\text{STAT1}}$ and $\overline{\text{STAT2}}$ pins of the AAT3673-1/-2/-4/-5 and the $\overline{\text{STAT1}}$ pin of the AAT3673-3/-6. A 2mA bias current should be sufficient to drive most low cost green or red LEDs. It is not recommended to exceed 8mA when driving an individual status LED.

The required ballast resistor value can be estimated using the following formulas:

When connecting to the adapter supply with a red LED:

$$R_{B(\overline{\text{STAT1,2}})} = \frac{V_{\text{ADP}} - V_{\text{FLED}}}{I_{\text{LED}(\overline{\text{STAT1,2}})}}$$

Example:

$$R_{B(\overline{\text{STAT1,2}})} = \frac{5.5\text{V} - 2.0\text{V}}{2\text{mA}} = 1.75\text{k}\Omega$$

Red LED forward voltage (V_F) is typically 2.0V @ 2mA.

When connecting to the USB supply with a green LED:

$$R_{B(\overline{\text{STAT1,2}})} = \frac{V_{\text{USB}} - V_{\text{FLED}}}{I_{\text{LED}(\overline{\text{STAT1,2}})}}$$

Example:

$$R_{B(\overline{\text{STAT1,2}})} = \frac{5.0\text{V} - 3.2\text{V}}{2\text{mA}} = 900\Omega$$

Green LED forward voltage (V_F) is typically 3.2V @ 2mA.

Protection Circuitry

Thermal Loop Control

Due to the integrated nature of the linear charging control pass devices for both the adapter and USB modes, a special thermal loop control system has been employed to maximize charging current under all operating conditions.

The thermal management system measures the internal circuit die temperature and reduces the charge current when the device exceeds a preset internal temperature control threshold. Once the thermal loop control becomes active, the constant charge current is initially reduced by a factor of 0.44.

The initial thermal loop current can be estimated by the following equation:

$$\text{Constant Charging: } I_{\text{TLOOP}} = I_{\text{CCADP}} \cdot 0.44$$

The thermal loop control re-evaluates the internal die temperature every three seconds and adjusts the fast charge current back up in small steps up to the full fast charge current level or until an equilibrium current is discovered and maximized for the given ambient temperature condition. In this manner, the thermal loop controls the system charge level. The AAT3673 will always provide the highest possible level of constant current in the fast charge mode for any given ambient temperature condition.

Programmable Watchdog Timer

The AAT3673 contains a watchdog timing circuit which operates in all charging modes. Typically a 0.1μF ceramic capacitor is connected between the CT pin and ground. When a 0.1μF ceramic capacitor is used, the device will time a shutdown condition if the trickle charge mode exceeds 50 minutes. When the device transitions from the trickle charge to the fast charge constant current mode and then to the constant voltage mode, the timer counting is continuous but the timeout value changes from 50 minutes to 7 hours.

Summary for a 0.1μF used for the timing capacitor:

Trickle Charge (TC) time out = 50 minutes

Trickle Charge (TC) + Fast Charge Constant Current (CC) + Constant Voltage (CV) mode time out = 7 hours

The CT pin is driven by a constant current source and will provide a linear response to increases in the timing capacitor value. Thus, if the timing capacitor were to be doubled from the nominal 0.1μF value, the time out time of the CC + CV modes would be doubled. The corresponding trickle charge time out time would be the combined CC + CV time divided by 8.

If the programmable watchdog timer function is not needed it may be disabled by terminating the CT pin to ground. The CT pin should not be left floating or not terminated; this will cause errors in the internal timing control circuit.

The counter input frequency will be divided by two in the event of a fault condition. Such fault conditions include digital thermal loop charge current reduction, battery charge reduction, and battery current sharing with the output during the charging cycle. When the fault condition recovers, the counter will resume the timing function.

The charge timer will automatically reset when the AAT3673 enable pin is reset or cycled off and on. The constant current provided to charge the timing capacitor is very small and this pin is susceptible to noise and changes in capacitance value. Therefore, the timing capacitor should be physically located on the printed circuit board layout as close as possible to the CT pin. Since the accuracy of the internal timer is determined by the capacitance value, a 10% tolerance or better ceramic capacitor is recommended. Ceramic capacitor materials such as X7R and X5R type are a good choice for this application.

Battery Over-Voltage Protection

An over-voltage event is defined as a condition where the voltage on the BAT pin exceeds the maximum battery charge voltage and is set by the over-voltage protection threshold (V_{BOVP}). If an over-voltage condition occurs, the AAT3673 charge control will shutdown the device until voltage on the BAT pin drops below the over-voltage protection threshold (V_{BOVP}). The AAT3673 will resume normal charging operation once the battery over-voltage condition is removed.

Over-Temperature Shutdown

The AAT3673 has a thermal protection control circuit which will shut down charging functions should the internal die temperature exceed the preset thermal limit threshold. Thermal shutdown also turns off the switches from ADP to OUT and BAT to OUT.

Battery Temperature Fault Monitoring

In the event of a battery over- or under-temperature condition, the charge control will turn off the internal charge path regulation device and disable the BAT-OUT dynamic path. After the system recovers from a temperature fault, the device will resume charging operation. The AAT3673 checks battery temperature before starting the charge cycle, as well as during all stages of charging. Typically, batteries employ the use of a negative temperature coefficient (NTC) thermistor that is integrated into the battery.

Capacitor Selection

Input Capacitor

A 1 μ F or larger capacitor is typically recommended for C_{ADP} . C_{ADP} should be located as close to the device ADP pin as practically possible. Ceramic, tantalum, or aluminum electrolytic capacitors may be selected for C_{ADP} . There is no specific capacitor equivalent series resistance (ESR) requirement for C_{ADP} . However, for higher current operation, ceramic capacitors are recommended for C_{ADP} due to their inherent capability over tantalum capacitors to withstand input current surges from low impedance sources such as batteries in portable devices.

Typically, 50V rated capacitors are required for most of the application to prevent any surge voltage. Ceramic capacitors selected as small as 1206 are available which can meet these requirements. Other voltage rating capacitor can also be used for the known input voltage application.

Charger Output Capacitor

The AAT3673 only requires a 1 μ F ceramic capacitor on the BAT pin to maintain circuit stability. This value should be increased to 10 μ F or more if the battery connection is made any distance from the charger output.

System Power Output Capacitor

For proper load voltage regulation and operational stability, a capacitor is required between OUT and GND. The output capacitor connection to the ground pin should be made as directly as practically possible for maximum device performance. Since the regulator has been designed to function with very low ESR capacitors, a 10 μ F ceramic capacitor is recommended for best performance.

Printed Circuit Board Layout Recommendations

For proper thermal management and to take advantage of the low $R_{DS(ON)}$ of the AAT3673, a few circuit board layout rules should be followed: IN and BAT should be routed using wider than normal traces, and GND should be connected to a ground plane. To maximize package thermal dissipation and power handling capacity of the AAT3673 TDFN4x4 package, solder the exposed paddle of the IC onto the thermal landing of the PCB, where the thermal landing is connected to the ground plane. If heat is still an issue, multi-layer boards with dedicated ground planes are recommended. Also, adding more thermal vias on the thermal landing would help transfer heat to the PCB effectively.

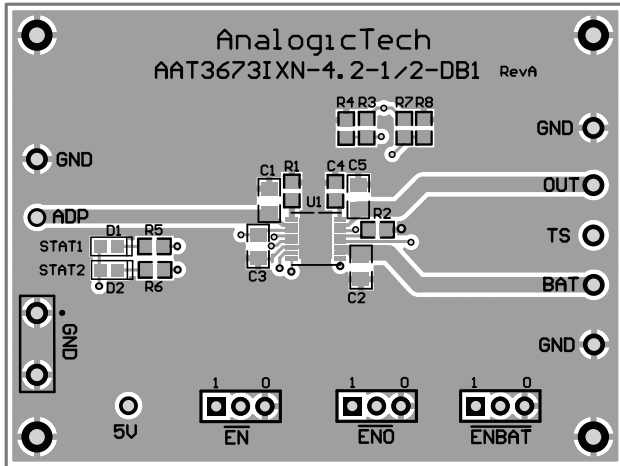


Figure 4: AAT3673-1/-2/-4/-5 Evaluation Board Top Layer.

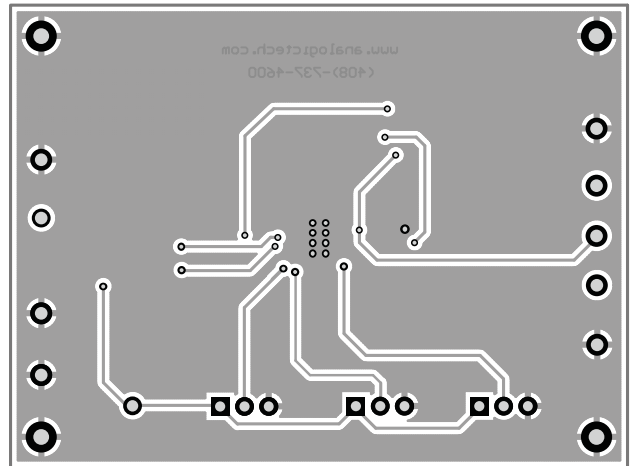


Figure 5: AAT3673-1/-2/-4/-5 Evaluation Board Bottom Layer.

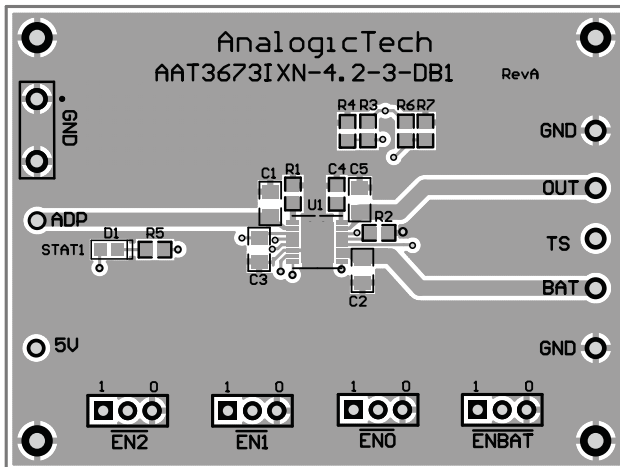


Figure 6: AAT3673-3/-6 Evaluation Board Top Layer.

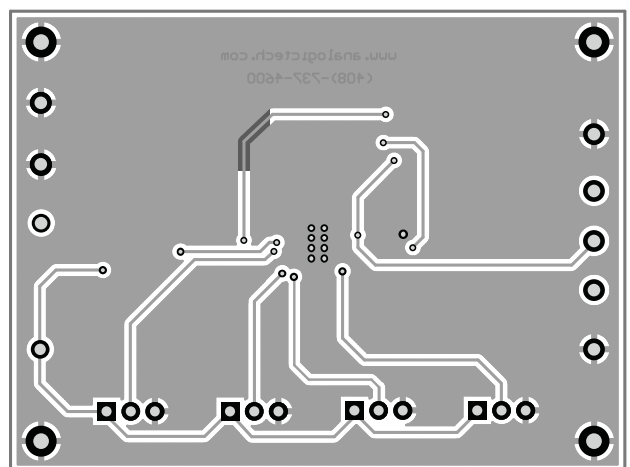


Figure 7: AAT3673-3/-6 Evaluation Board Bottom Layer.

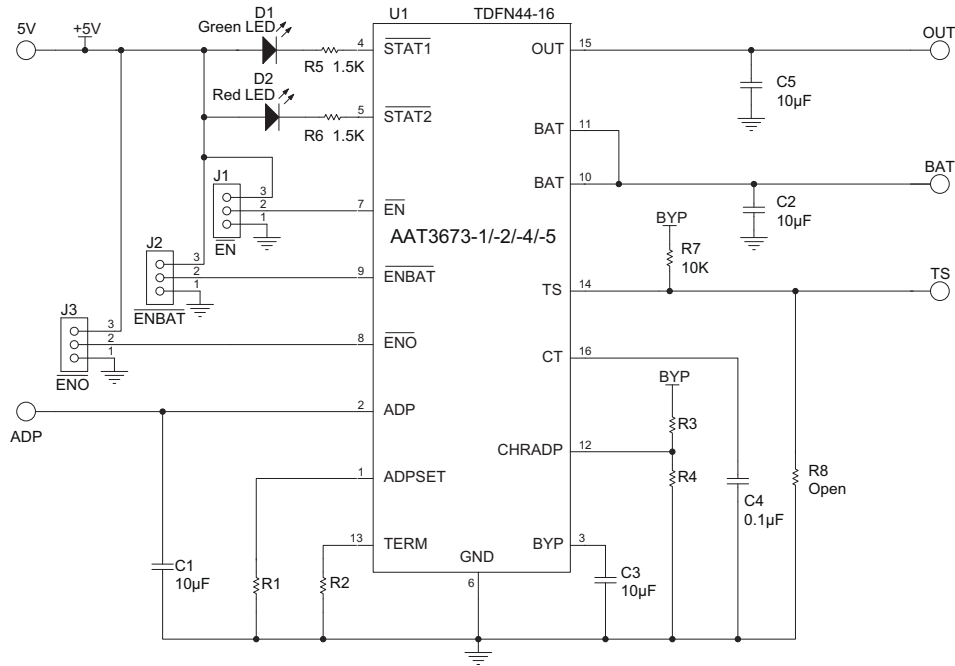


Figure 8: AAT3673-1/-2/-4/-5 Evaluation Board Schematic.

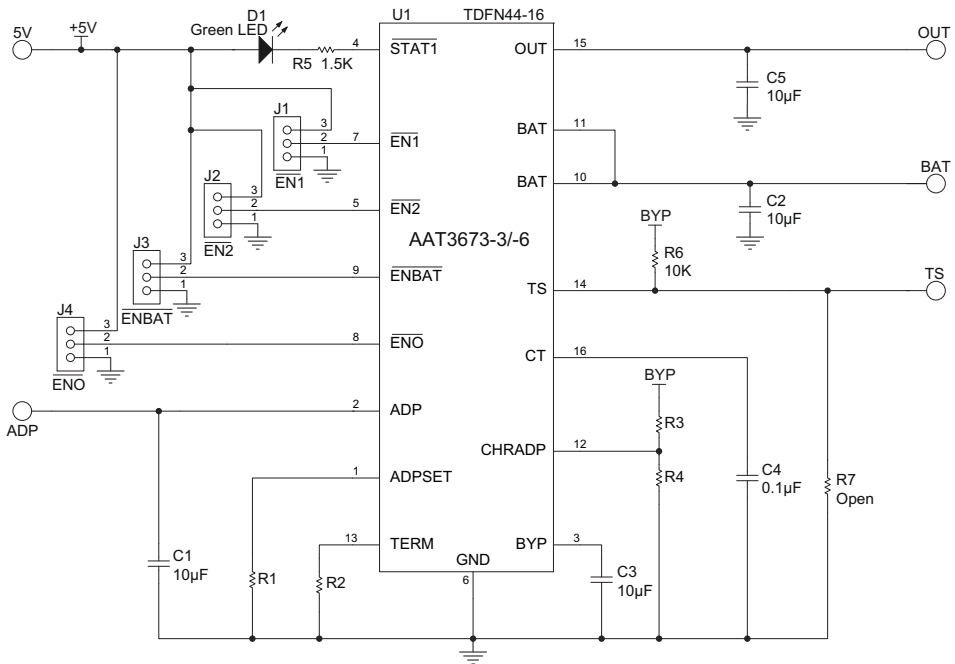


Figure 9: AAT3673-3/-6 Evaluation Board Schematic.

Component	Part Number	Description	Manufacturer
U1	AAT3673-1/-2/-4/-5IXN	1.6A Linear Li-Ion/Polymer Battery Charger in TDFN4x4-16 Package	AnalogicTech
R1	Chip Resistor	57.6kΩ, 1%, 1/4W; 0603	Vishay
R2	Chip Resistor	71.5kΩ, 5%, 1/4W; 0603	Vishay
R5, R6	Chip Resistor	1.5kΩ, 5%, 1/4W; 0603	Vishay
R7	Chip Resistor	10kΩ, 5%, 1/4W; 0603	Vishay
C4	GRM188R61A225KE34	0.1μF 10V 10% X5R 0603	Murata
C1, C2, C3, C5	GRM21BR71A106KE51L	10μF 10V 10% X7R 0805	Murata
J1, J2, J3	PRPN401PAEN	Conn. Header, 2mm zip	Sullins Electronics
D1	LTST-C190GKT	Green LED; 0603	Lite-On Inc.
D2	LTST-C190CKT	Red LED; 0603	Lite-On Inc.

Table 9: AAT3673-1/-2/-4/-5 Evaluation Board Bill of Materials (BOM).

Component	Part Number	Description	Manufacturer
U1	AAT3673-3/-6IXN	1.6A Linear Li-Ion/Polymer Battery Charger in TDFN4x4-16 Package	AnalogicTech
R1	Chip Resistor	57.6kΩ, 1%, 1/4W; 0603	Vishay
R2	Chip Resistor	71.5kΩ, 5%, 1/4W; 0603	Vishay
R5	Chip Resistor	1.5kΩ, 5%, 1/4W; 0603	Vishay
R6	Chip Resistor	10kΩ, 5%, 1/4W; 0603	Vishay
C4	GRM188R61A225KE34	0.1μF 10V 10% X5R 0603	Murata
C1, C2, C3, C5	GRM21BR71A106KE51L	10μF 10V 10% X7R 0805	Murata
J1, J2, J3, J4	PRPN401PAEN	Conn. Header, 2mm zip	Sullins Electronics
D1	LTST-C190GKT	Green LED; 0603	Lite-On Inc.

Table 10: AAT3673-3/-6 Evaluation Board Bill of Materials (BOM).

Product	Constant Voltage Regulation (V)	OVP Trip Point (V)	OVP Turn On Delay Time (μs)	Preconditioning Voltage Threshold (V)	Number of Status Pins	Number of Enable Pins
AAT3673-1	4.2	6.75	130	2.9	2; see Table 6	1; see Table 1
AAT3673-2	4.2	6.75	130	2.9	2; see Table 7	1; see Table 1
AAT3673-3	4.2	6.75	130	2.9	1; see Table 8	2; see Table 2
AAT3673-4	4.2	6.75	80,000	2.9	2; see Table 6	1; see Table 1
AAT3673-5	4.2	6.75	80,000	2.9	2; see Table 7	1; see Table 1
AAT3673-6	4.2	6.75	80,000	2.9	1; see Table 8	2; see Table 2

Table 11: AAT3673 Options.

Ordering Information

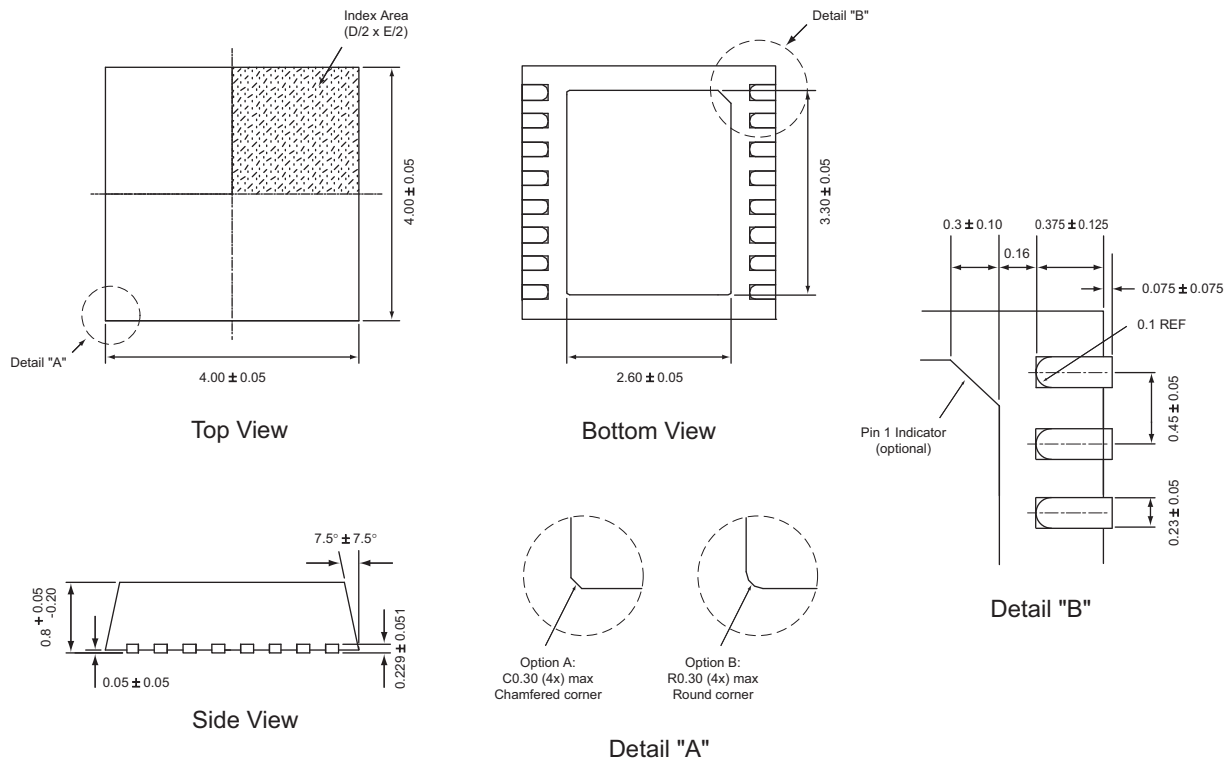
Package	Marking ¹	Part Number (Tape and Reel) ²
TDFN44-16	9SXY	AAT3673IXN-4.2-1-T1
TDFN44-16	9XXYY	AAT3673IXN-4.2-2-T1
TDFN44-16	8SXY	AAT3673IXN-4.2-3-T1
TDFN44-16		AAT3673IXN-4.2-4-T1
TDFN44-16		AAT3673IXN-4.2-5-T1
TDFN44-16		AAT3673IXN-4.2-6-T1



All AnalogicTech products are offered in Pb-free packaging. The term “Pb-free” means semiconductor products that are in compliance with current RoHS standards, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. For more information, please visit our website at <http://www.analogictech.com/about/quality.aspx>.

Packaging Information

TDFN44-16³



1. XYY = assembly and date code.
 2. Sample stock is generally held on part numbers listed in **BOLD**.
 3. The leadless package family, which includes QFN, TQFN, DFN, TDFN and STDFN, has exposed copper (unplated) at the end of the lead terminals due to the manufacturing process. A solder fillet at the exposed copper edge cannot be guaranteed and is not required to ensure a proper bottom solder connection.

**BatteryManager™*****1.6A Dynamic Battery Charger and Power Management IC***

Advanced Analogic Technologies, Inc.
3230 Scott Boulevard, Santa Clara, CA 95054
Phone (408) 737-4600
Fax (408) 737-4611



© Advanced Analogic Technologies, Inc.
AnalogicTech cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in an AnalogicTech product. No circuit patent licenses, copyrights, mask work rights, or other intellectual property rights are implied. AnalogicTech reserves the right to make changes to their products or specifications or to discontinue any product or service without notice. Except as provided in AnalogicTech's terms and conditions of sale, AnalogicTech assumes no liability whatsoever, and AnalogicTech disclaims any express or implied warranty relating to the sale and/or use of AnalogicTech products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards. Testing and other quality control techniques are utilized to the extent AnalogicTech deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed. AnalogicTech and the AnalogicTech logo are trademarks of Advanced Analogic Technologies Incorporated. All other brand and product names appearing in this document are registered trademarks or trademarks of their respective holders.